

UNITED STATES PATENT AND TRADEMARK OFFICE  
**CERTIFICATE OF CORRECTION**

PATENT NO. : 6,933,780 B2  
DATED : August 23, 2005  
INVENTOR(S) : Fujiwara et al.

Page 1 of 1

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

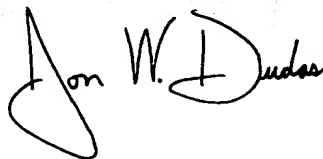
Title page.

Item [57], **ABSTRACT**, should read:

A predistortion circuit has an input terminal for inputting a predetermined signal; a nonlinear device directly or indirectly connected to the input terminal; a bias supply circuit for applying a voltage to the nonlinear device; a specific-frequency suppressing device connected to one side or both sides of the nonlinear device directly without another intervening device for suppressing all or part of such frequencies that are from a frequency corresponding to DC to a frequency corresponding to an occupied band width of an input signal inputted to the input terminal and/or suppressing at least one higher harmonic frequency of a carrier wave of the input signal; and an output terminal for outputting a signal.

Signed and Sealed this

Twenty-first Day of February, 2006

A handwritten signature in black ink, appearing to read "Jon W. Dudas". The signature is stylized with a large, looping initial "J" and a distinct "D".

JON W. DUDAS  
*Director of the United States Patent and Trademark Office*